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APPLICATION NO.

09/927134

Robert Ogle Arvind Halliyal

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CLASS	SUBCLASS	ART UNIT	EXAMINER	
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Process for treating ONO dielectric film of a floating gate memory cell

ISSUING CLASSIFICATION

ORIGINAL CROSS REFERENCE(S)

CLASS SUBCLASS CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

COntinued on issue Slip inside File Jacket

TERMINAL	DRAWINGS			CLAIMS ALLOWED	
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
The term of this patent				NOTICE OF AL	LOWANCE MAILED
subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)		
The term of this patent shall not extend beyond the expiration date					
of U.S Patent. No.				ISSUE FEE	
				Amount Due	Date Paid
	(Primary	Examiner)	(Date)		
The terminalmonths of				ISSUE BA	TCH NUMBER
this patent have been disclaimed.	(Legal Instrum	ents Examiner)	(Date)		
WARNING:					
The information disclosed herein may be res Possession outside the U.S. Patent & Trader					35, Sections 122, 181 and 368
orm PTO-436A Rev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RO
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